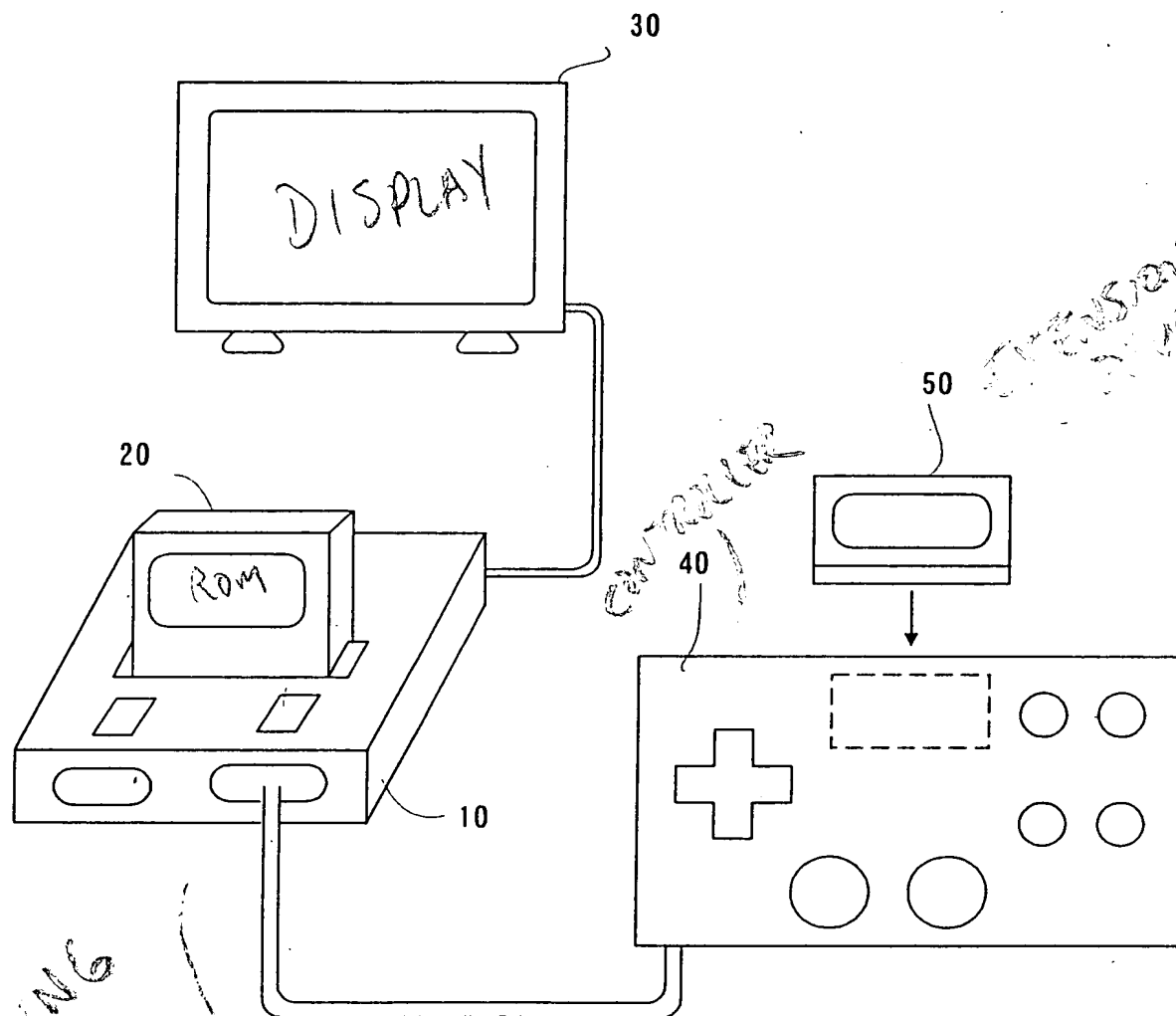


APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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FIG. 1



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DRIVING
FOR
DATA

controller

display

FIG. 2

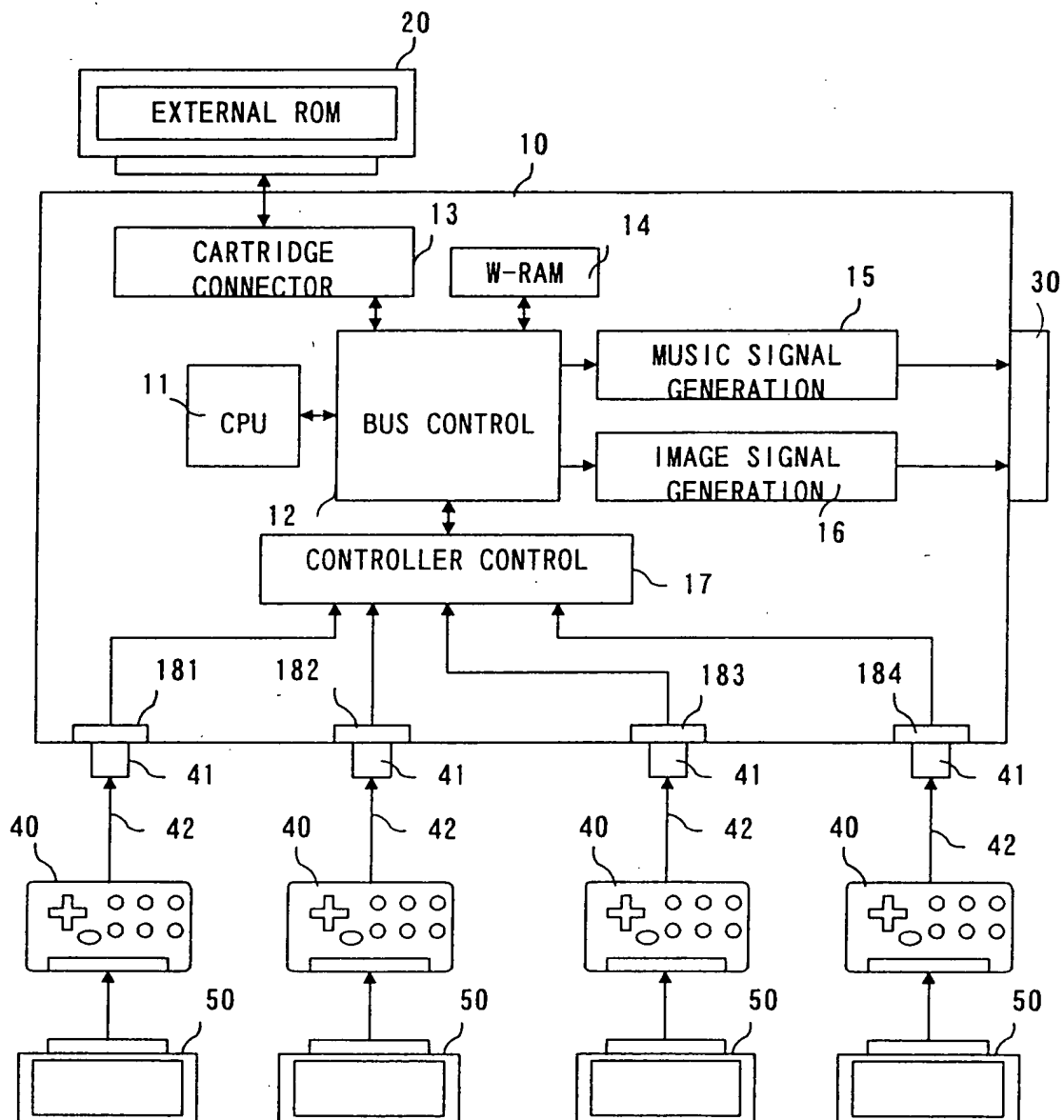


FIG. 3

CPU MEMORY MAP

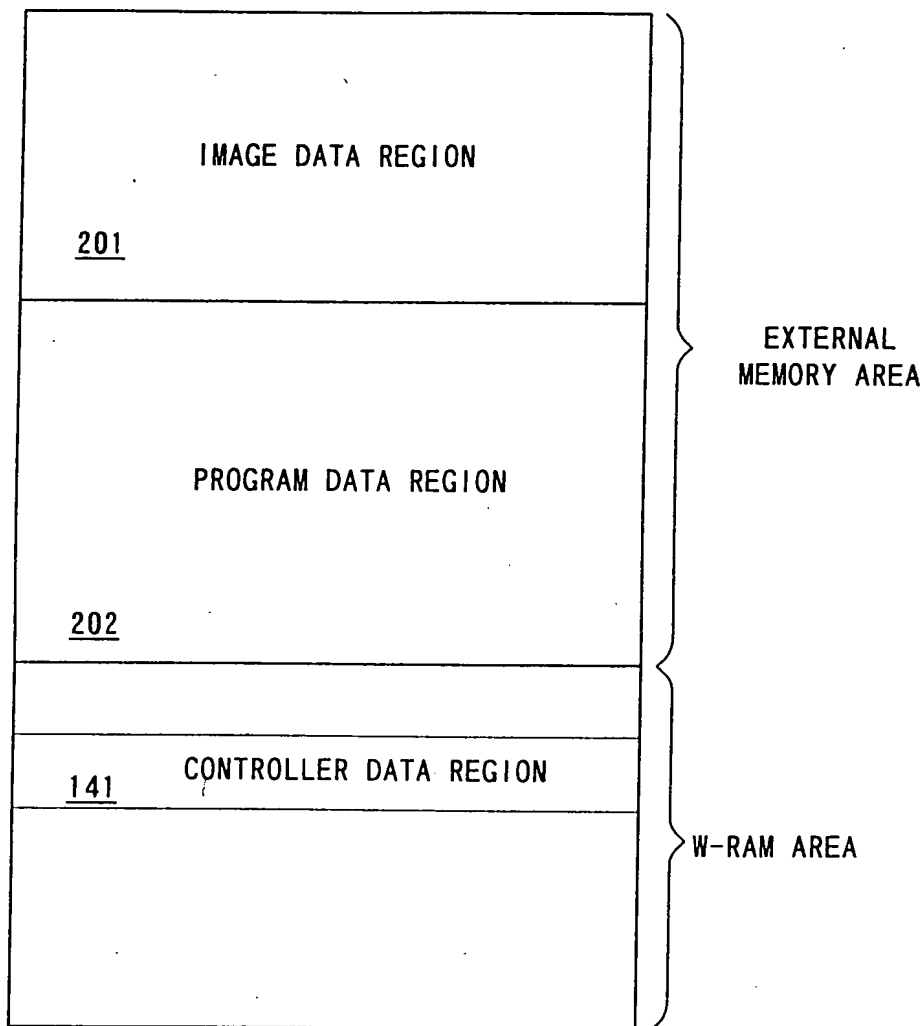
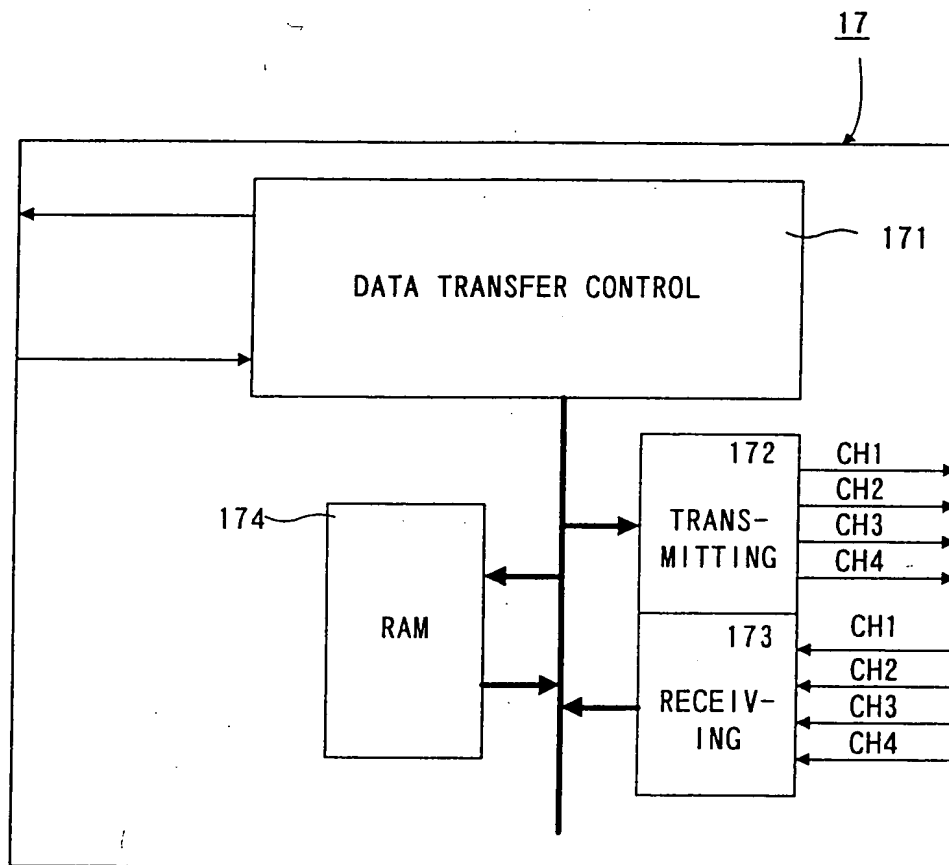


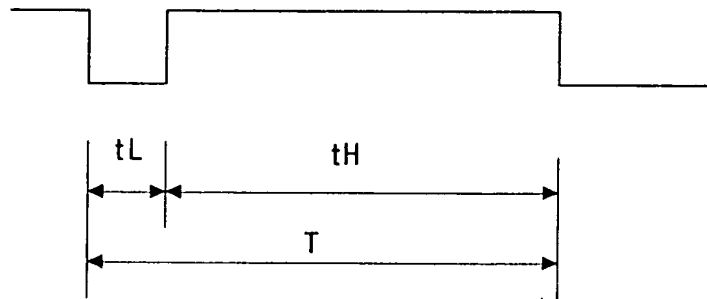
FIG. 4



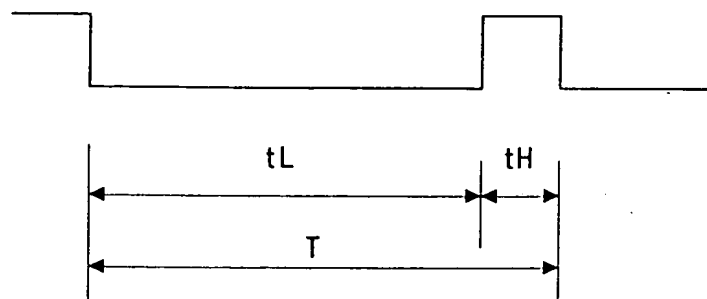
APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 5

"1" SIGNAL



"0" SIGNAL



1CH COMMAND STORAGE POSITION		174a
1CH TRANSMISSION/RECEPTION DATA STORAGE POSITION		
174b		
2CH COMMAND STORAGE POSITION		174c
2CH TRANSMISSION/RECEPTION DATA STORAGE POSITION		
174d		
3CH COMMAND STORAGE POSITION		174e
3CH TRANSMISSION/RECEPTION DATA STORAGE POSITION		
174f		
4CH COMMAND STORAGE POSITION		174g
4CH TRANSMISSION/RECEPTION DATA STORAGE POSITION		
174h		

FIG. 8

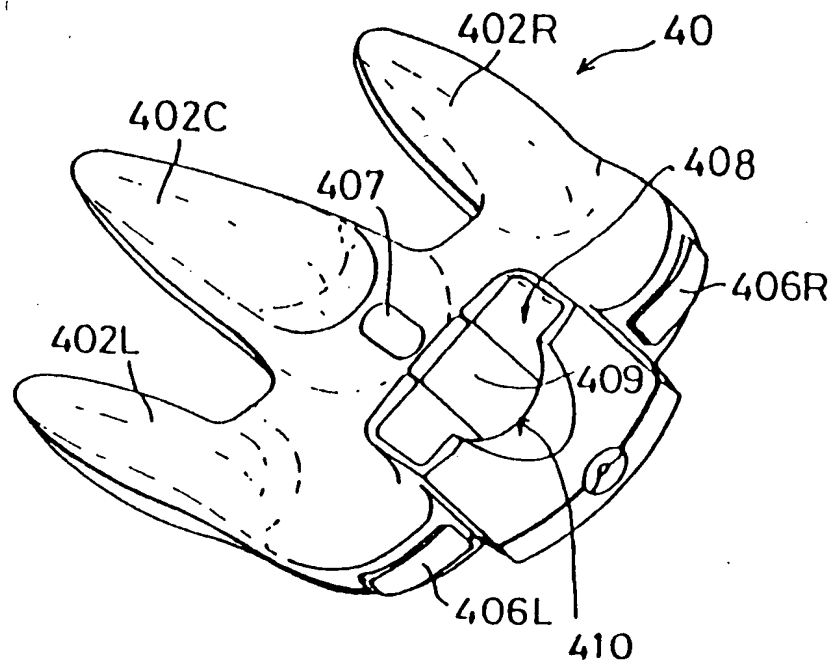
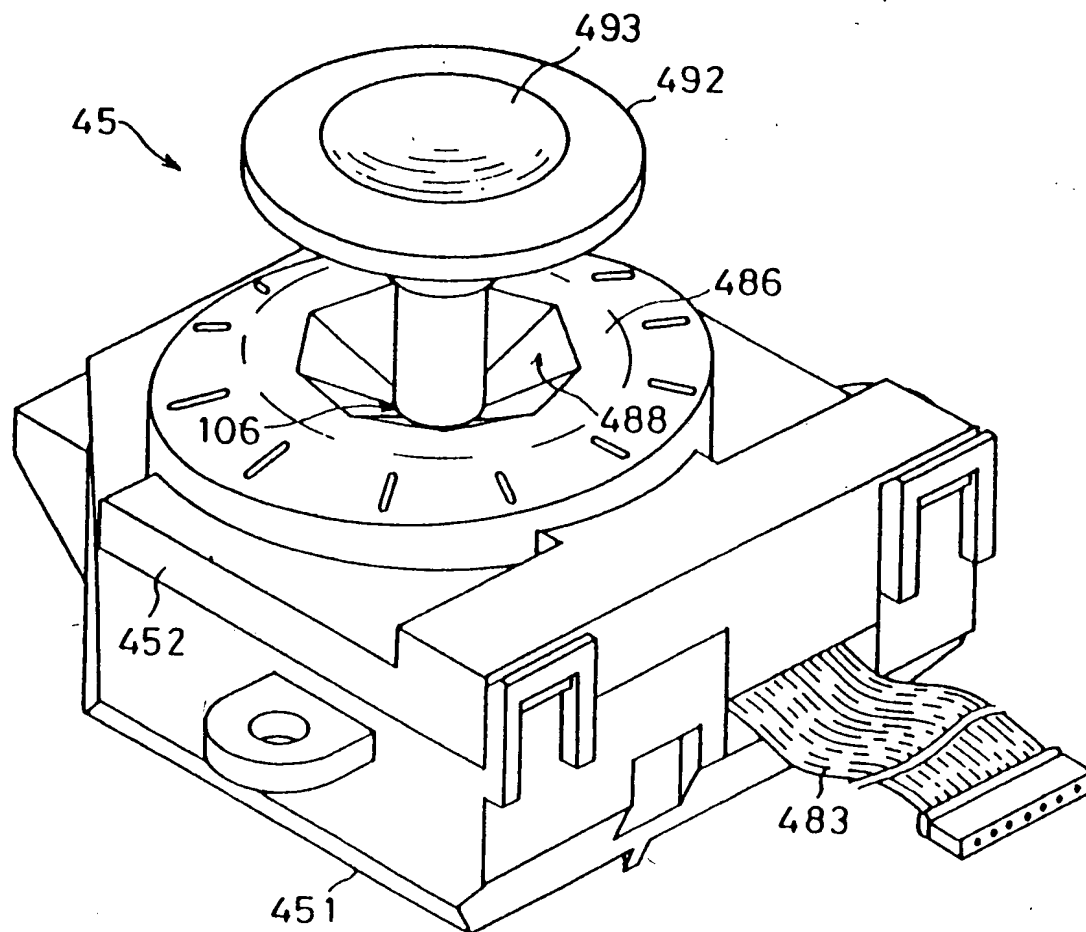
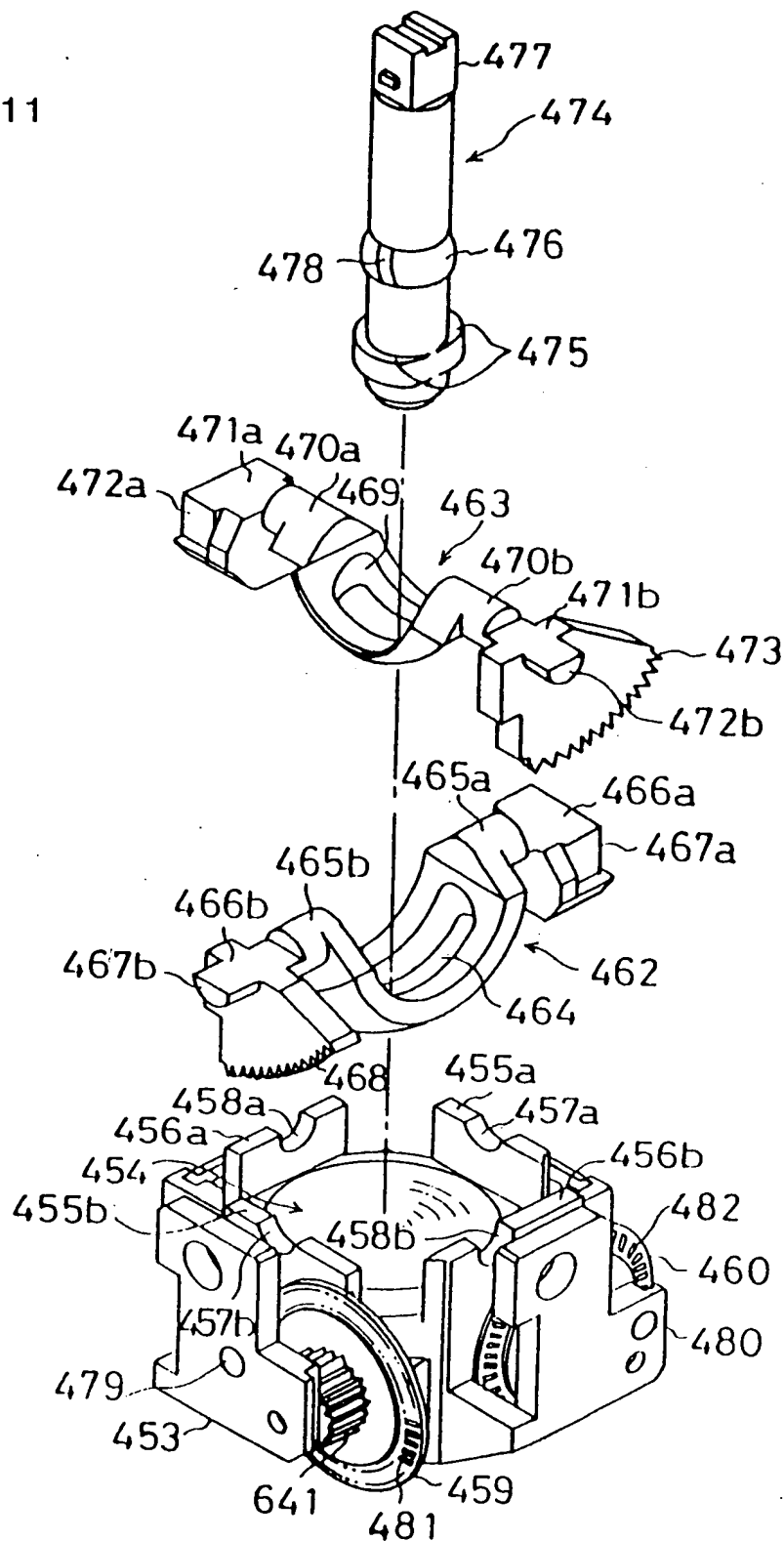


FIG. 9



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FIG. 11



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FIG. 12

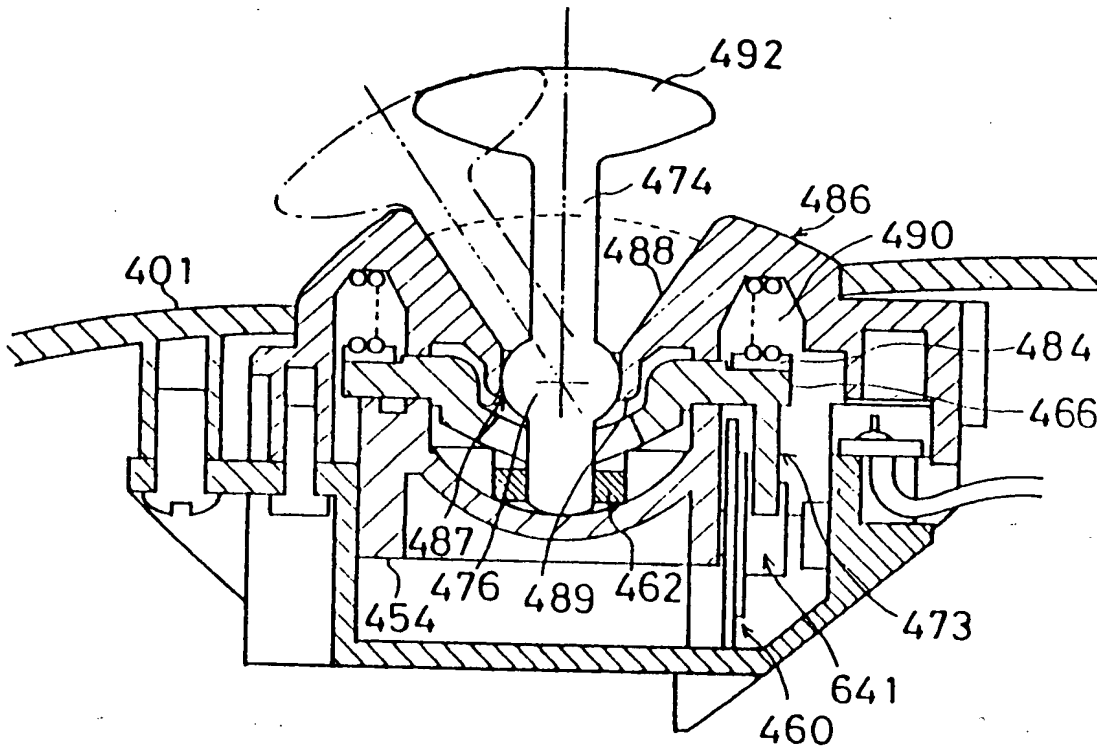


FIG. 13

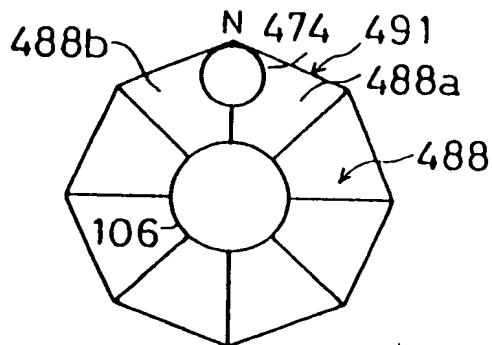
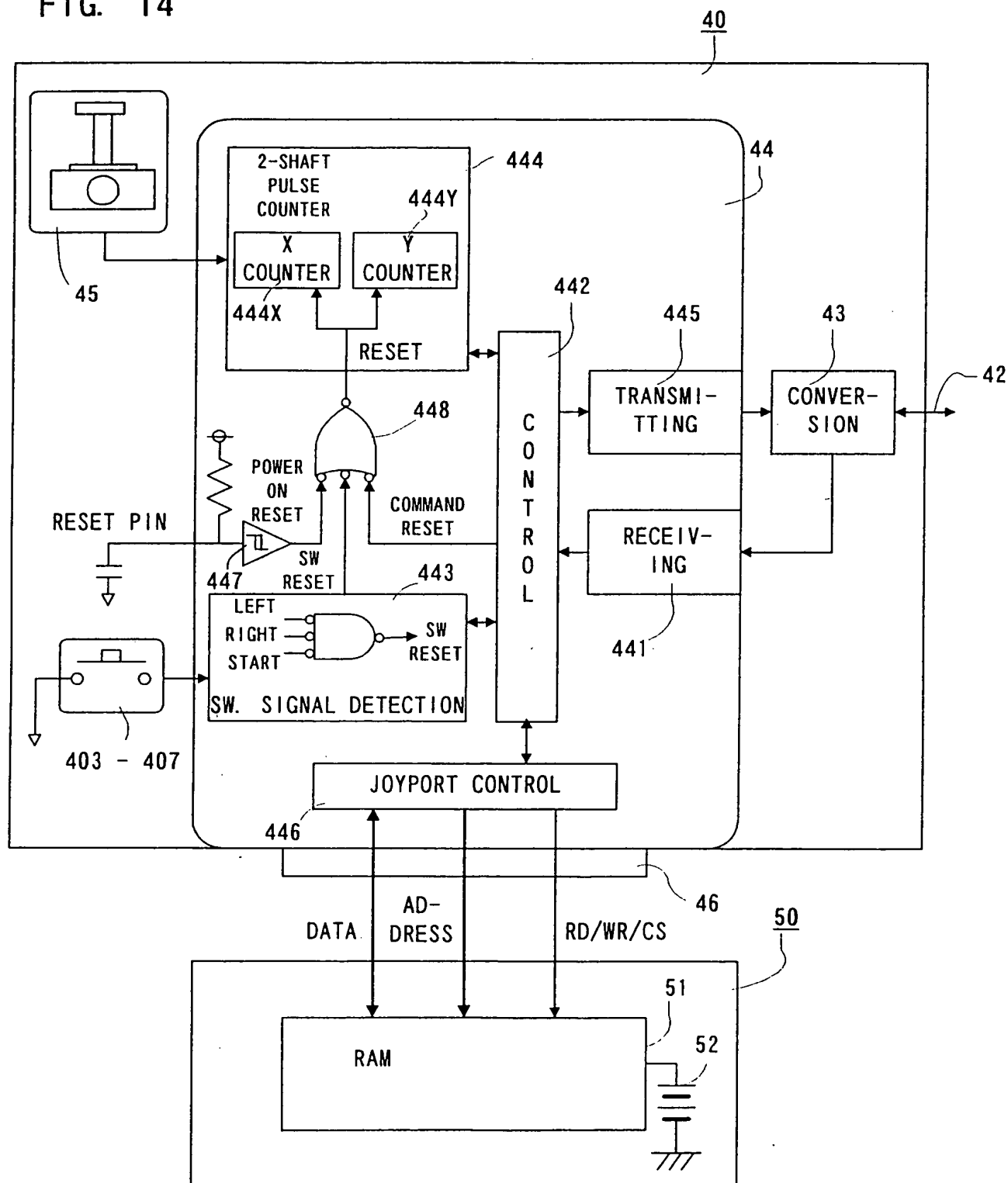


FIG. 14



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 15

1 BYTE	B	A	G	START	↑	↓	←	→
2 BYTE	JSRST	0	L	R	E	D	C	F
3 BYTE	<div> <div>X</div> <div>ORDINATE</div> </div>							
4 BYTE	<div> <div>Y</div> <div>ORDINATE</div> </div>							

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FIG. 16

COMMAND 0: TRANSMITTING TYPE OF CONTROLLER
 RECEPTION: 1 BYTE TRANSMISSION: 3 BYTES

		d7	d6	d5	d4	d3	d2	d1	d0
RECEP- TION	1 BYTE	← COMMAND 0 →							
	1 BYTE	← TYPE L →							
TRANS- MISSION	2 BYTE	← TYPE H →							
	3 BYTE	← STATUS →							

FIG. 17

COMMAND 1: ACCESSING STANDARD CONTROLLER
 RECEPTION: 1 BYTE TRANSMISSION: 4 BYTES

		d7	d6	d5	d4	d3	d2	d1	d0
RECEP- TION	1 BYTE	← COMMAND 1 →							
	1 BYTE	B	A	G	START	↑	↓	←	→
TRANS- MISSION	2 BYTE	JSRST*	0	L	R	E	D	C	F
	3 BYTE	← X ORDINATE →							
	4 BYTE	← Y ORDINATE →							

* HIGH LEVEL WHEN L, R AND START BUTTONS ARE
 SIMULTANEOUSLY DEPRESSED

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FIG. 18

COMMAND 2: READ-OUT RAM
RECEPTION: 3 BYTES TRANSMISSION: 33 BYTES

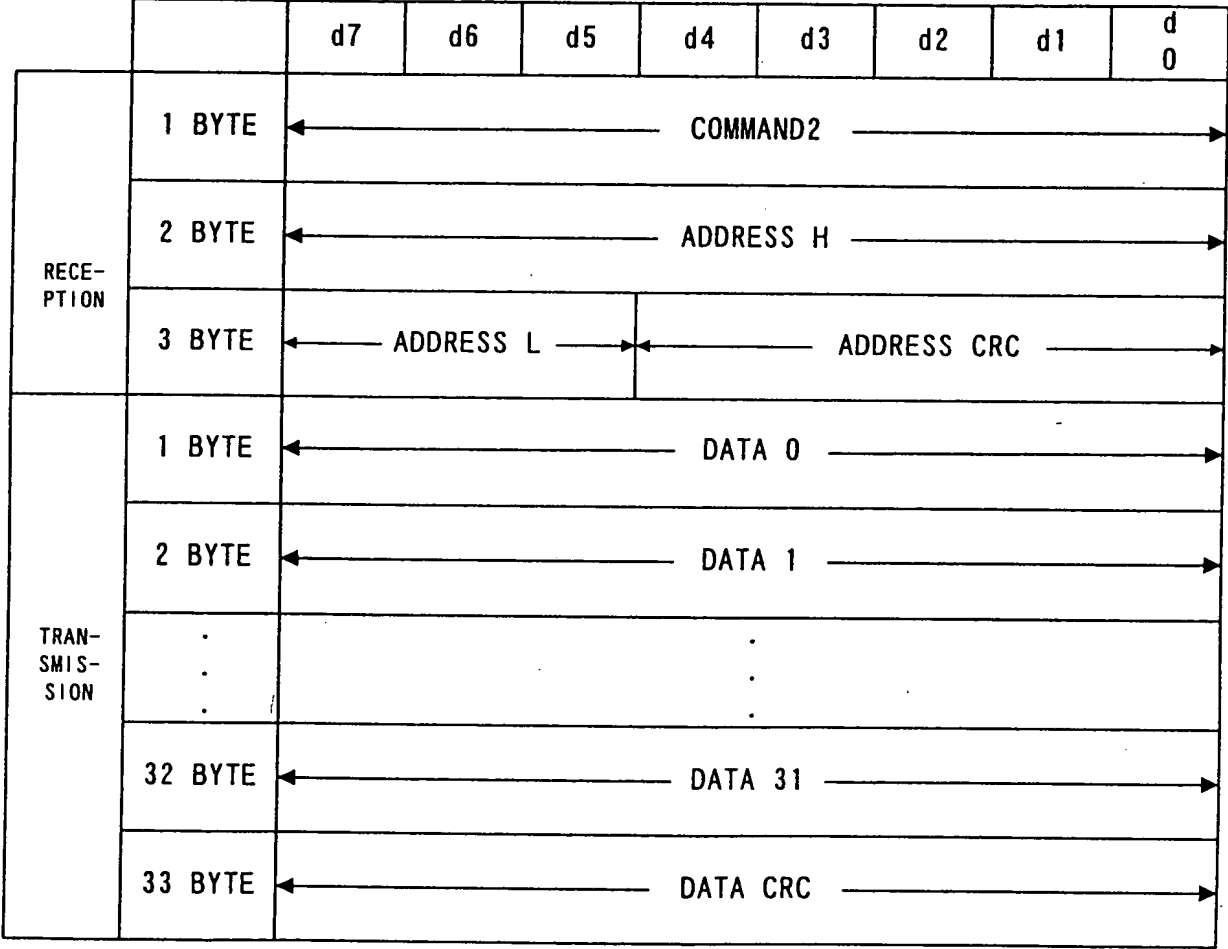
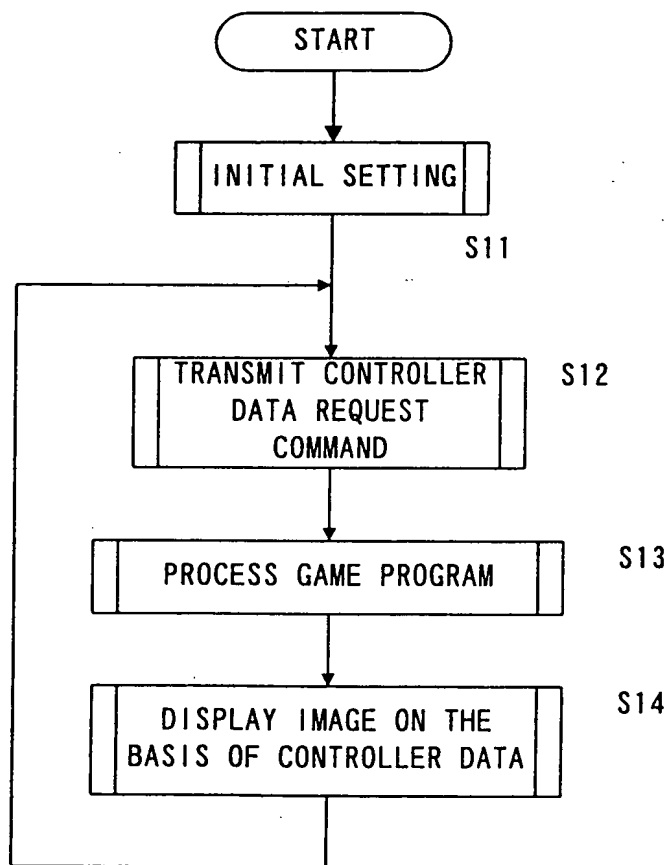


FIG. 19

FIG. 20



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[illegible]

```

graph TD
    Start([POWER ON]) --> S20[ ]
    S20 --> S21{IS THERE CONTROLLER DATA  
REQUEST COMMAND?}
    S21 -- NO --> S20
    S21 -- YES --> S22[INPUT COMMAND TO  
CONTROLLER CONTROL 17]
    S22 --> S23{HAS DATA ENTRY FROM CONTROLLER  
CONTROL 17 BEEN COMPLETED?}
    S23 -- NO --> S20
    S23 -- YES --> S24[TRANSFER DATA FROM  
CONTROLLER 17 TO  
W-RAM 14]
    S24 --> S20

```

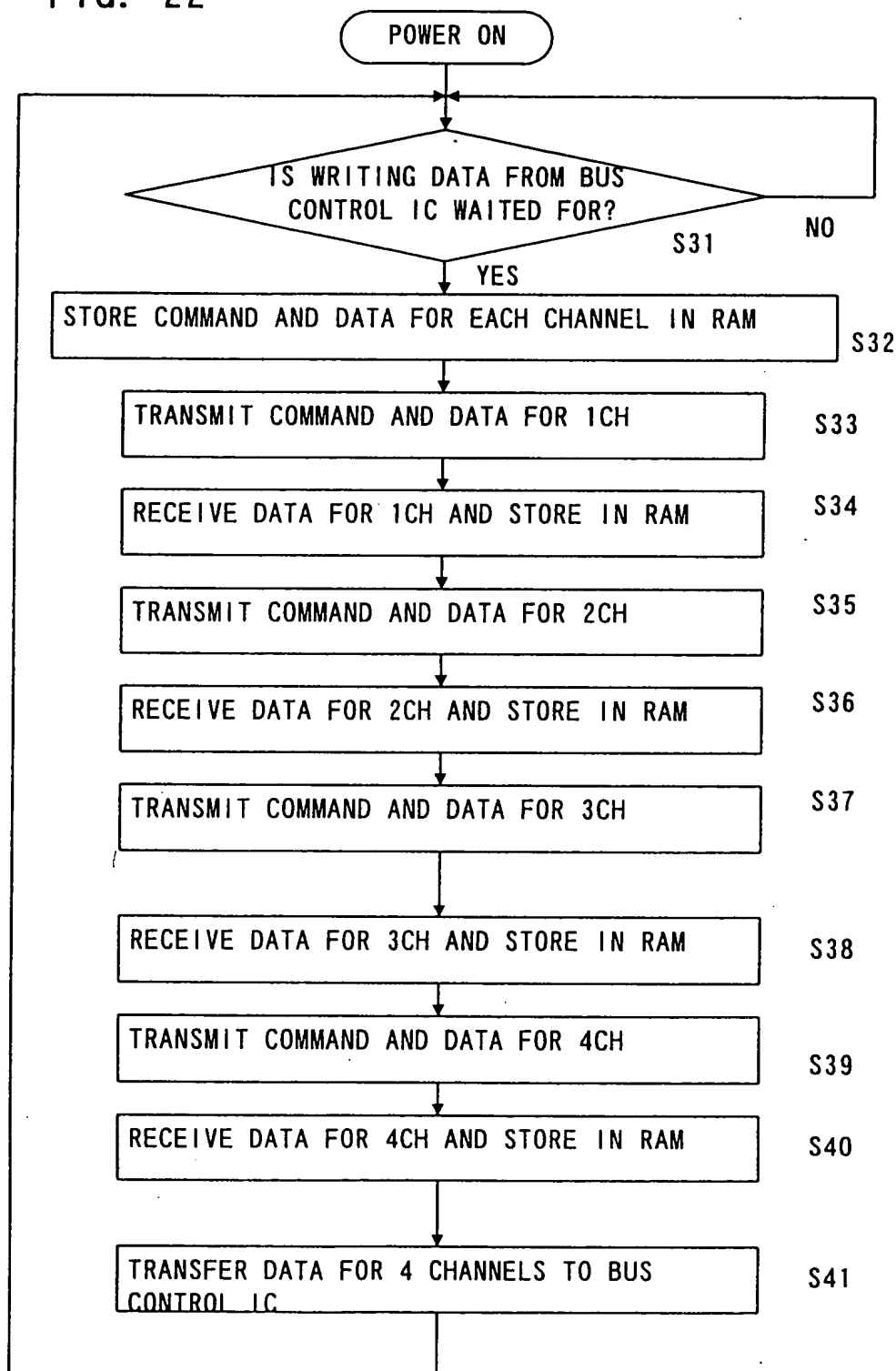


FIG. 23

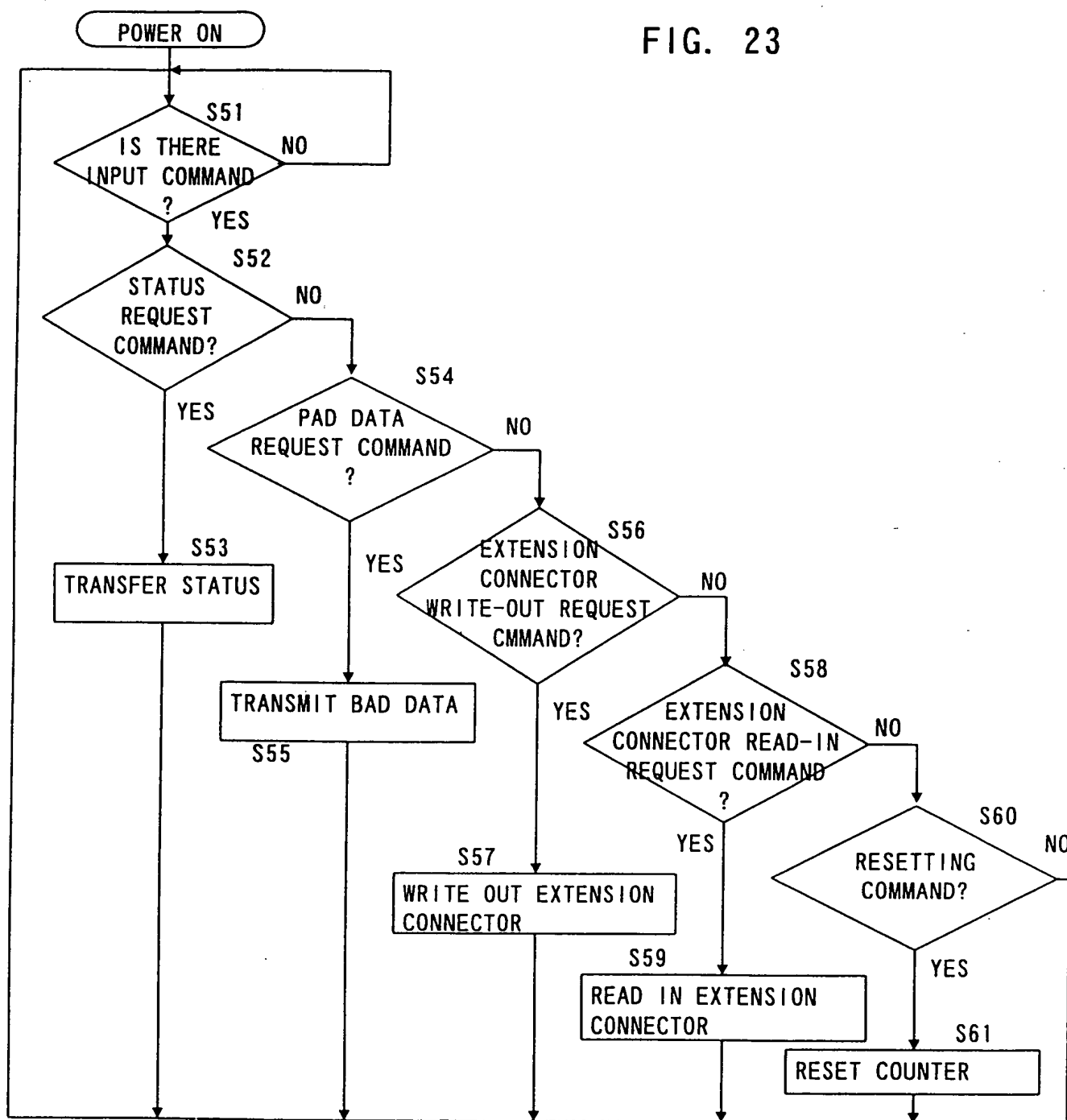


FIG. 24

FIG. 25

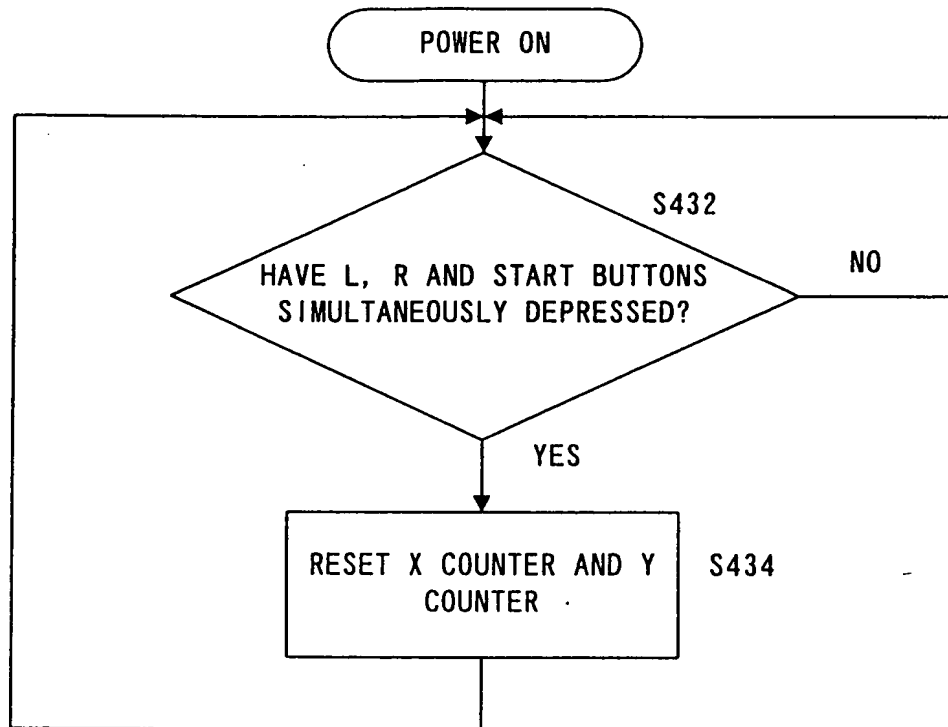


FIG. 26

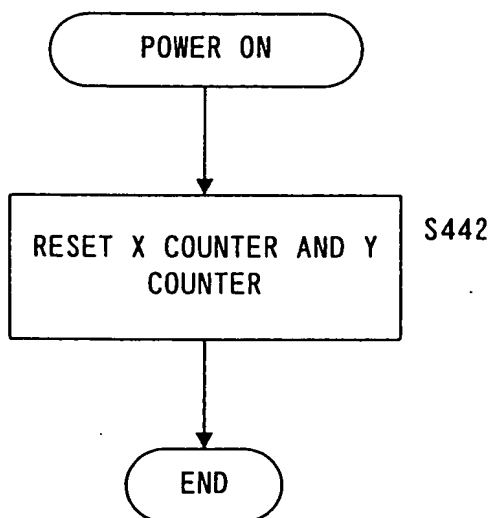
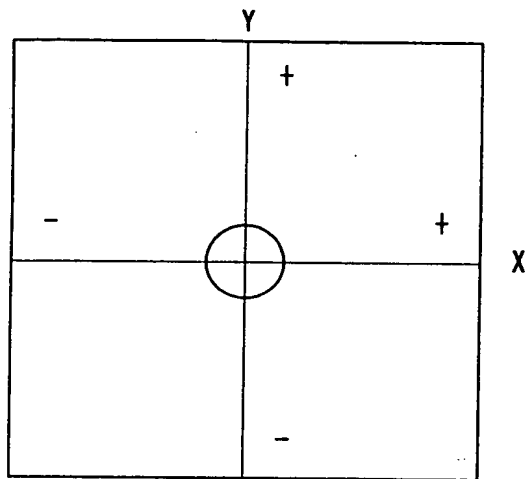


FIG. 27

PHYSICAL COORDINATE OF JOYSTICK



DISPLAY SCREEN

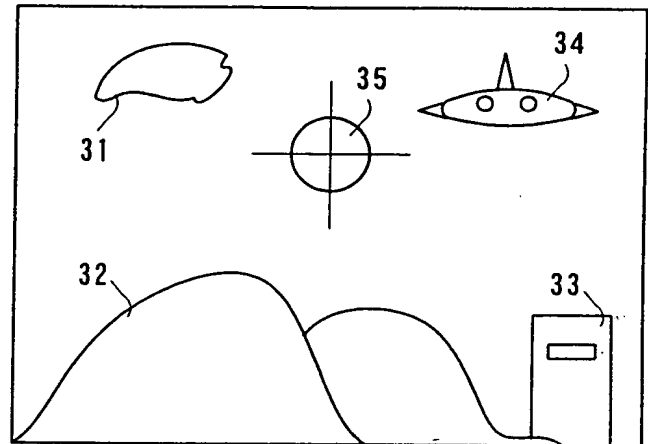
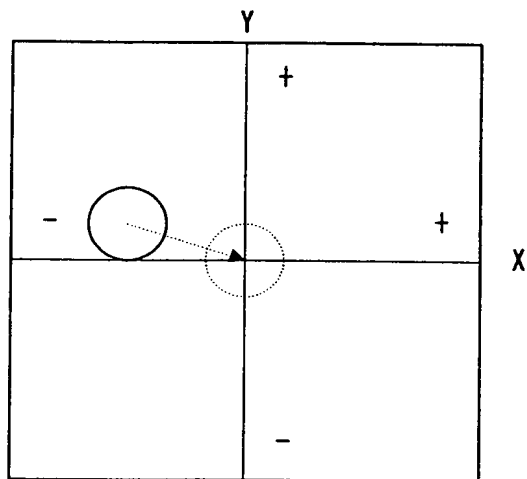


FIG. 28

PHYSICAL COORDINATE OF JOYSTICK



DISPLAY SCREEN

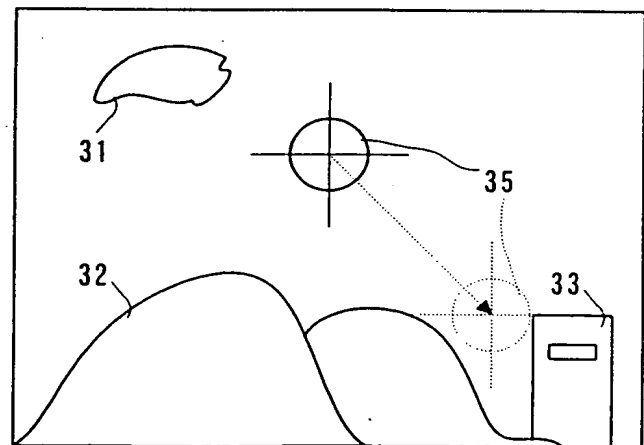


FIG. 29

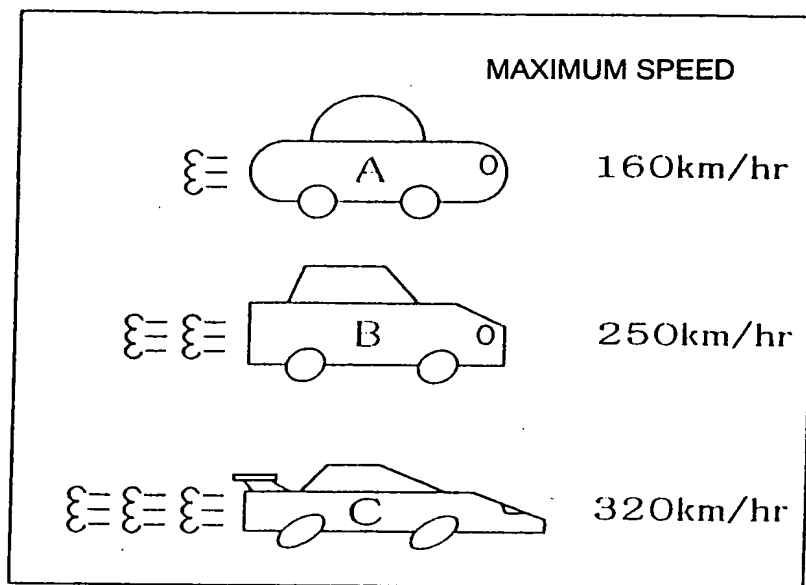
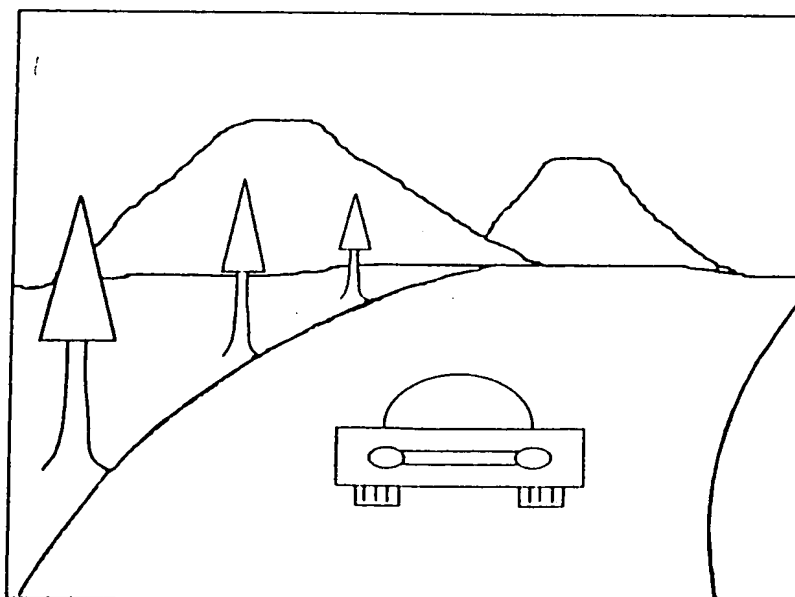


FIG. 30



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FIG. 31

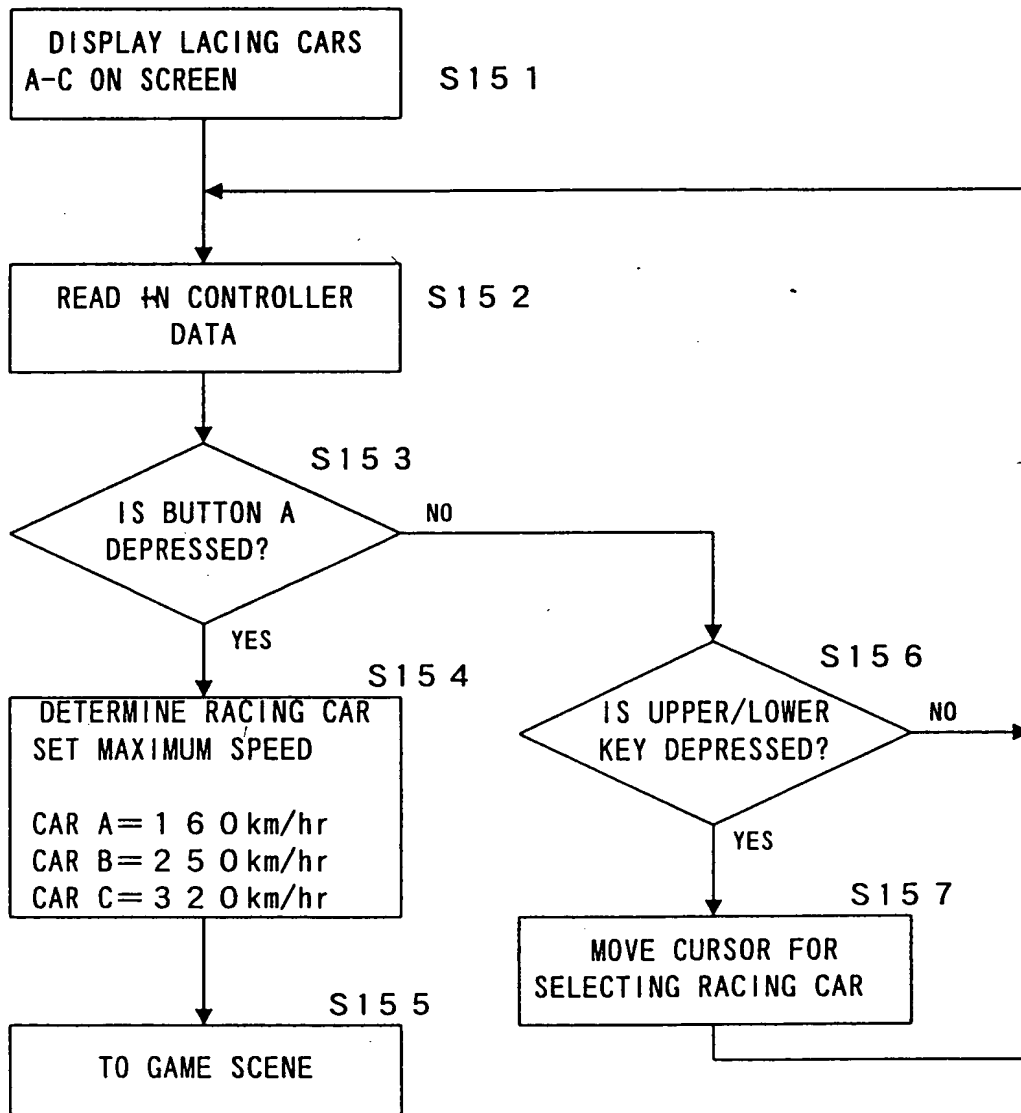
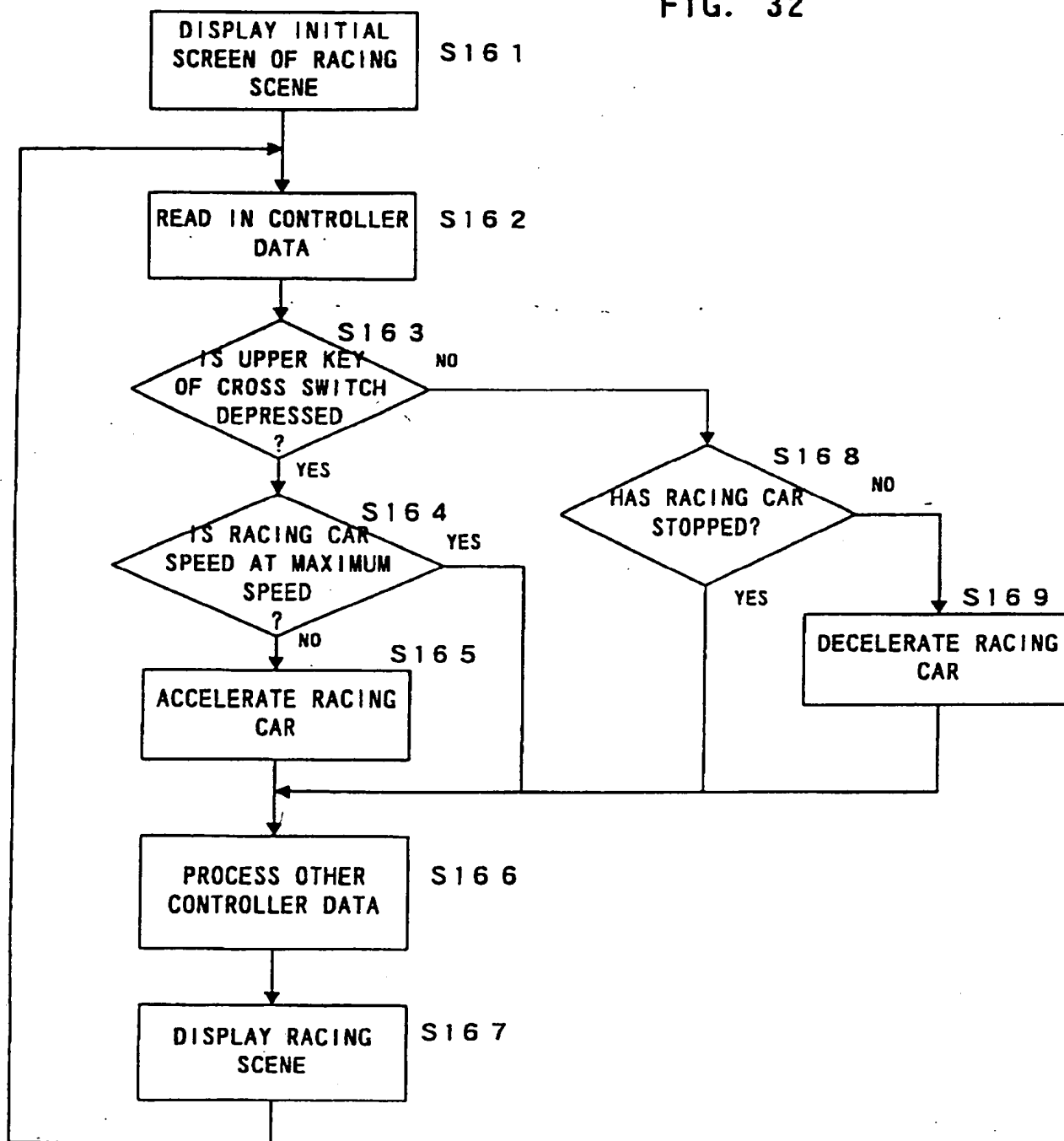


FIG. 32



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APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 34

